



# COEP Technological University Pune (COEP Tech)

A Unitary Public University of Government of Maharashtra

(Formerly College of Engineering Pune)

Wellesley Road, Shivajinagar, Pune-411005, Maharashtra, India

Tel: 020-25507000/09

Website: [www.coeptech.ac.in](http://www.coeptech.ac.in)

COEP\_TECH: Registrar Office/ANRF-PAIR Projects/Recruitment/March 2026/

Date: 16/03/2026

## **Detailed Advertisement for the project positions under the ANRF-PAIR project**

### **IIT-B COEP-TU Hub-Spoke model (RP/2025/COEPTech/SSM\_others/34 dt. 09/12/2025)**

Established in the year 1854, College of Engineering Pune (COEP) is the third oldest engineering institution in Asia, having glorious legacy of 170 years. COEP was bestowed with the status of “Unitary Technological Public University” in June 2022. It is now functional as COEP Technological University (COEP Tech). To reinforce the teaching, learning and research excellence at COEP Technological University, applications are invited for temporary Project Positions under the ANRF-PAIR Hub-Spoke model in collaboration with IIT Bombay. For more information about the project visit <https://anrfonline.in/ANRF/PAIR?HomePage=New>

**Last date of receiving application through Registered post/ courier or via Speed post: 24<sup>th</sup> April 2026**

#### **Summary of the Project titles, Research Positions, Qualification, Experience and Emoluments:**

Project UID No.	Project Title	Post Code (No. of Post)	Essential Qualification	Desirable Experience	Job Description & Responsibilities	Monthly Emoluments as per ANRF norms
01	Development of a Switched Reluctance Motor and Permanent Magnet Assisted Synchronous	01/RA-1 (No. of Post: 1)	Ph. D. completed/ nearing completion in Electrical / Electrical and Electronics / Instrumentation / Electronics engineering with quality publications OR	<ul style="list-style-type: none"><li>Proficiency in embedded systems and familiarity with various development platforms is essential.</li><li>Significant experience in dynamic modelling, simulation, and analysis of power electronics and electric machines/electromechanical</li></ul>	<ul style="list-style-type: none"><li>Design, prototype and execute experiments to validate PMA synchronous Reluctance motor and Switched reluctance motor.</li></ul>	Initially for 1 year (Extendable up to 4 years subject to satisfactory performance and fund availability). Year 1 to 5: ₹



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Reluctance Motor for Electric Tractor		<p>3 years of research, teaching and design and development experience after Master's degree (M.E./MTech/ M.S.) in Electrical / Electronics / Instrumentation engineering with at least one research paper in SCI journal.</p> <p>Preference will be given to candidate with valid GATE score.</p>	<p>systems.</p> <ul style="list-style-type: none"> <li>• Proficiency in simulation tools such as Ansys Maxwell and MATLAB/Simulink</li> <li>• Expertise in Electromagnetic FEA tools such as Ansys Maxwell, Motor-CAD, or Flux</li> <li>• Strong understanding of Power Electronics (e.g., inverters / converters, gate drivers, IGBTs, MOSFETs).</li> <li>• Expertise in Motor Control Theory, including Field-Oriented Control (FOC) and PID tuning.</li> </ul>	<ul style="list-style-type: none"> <li>• Perform thermal FEA studies and suggest suitable cooling for the motor.</li> <li>• Experience with control systems modelling and simulation tools (MATLAB/Simulink or equivalent).</li> <li>• Proven ability to troubleshoot and resolve complex hardware and software integration issues.</li> </ul>	58,000 /- + 30% HRA
	<p>01/JRF-1 (No. of Post: 1)</p>	<ul style="list-style-type: none"> <li>• B.E / B. Tech / M.E. / M. Tech in Electrical Engineering program with minimum first class.</li> </ul> <p>Preference will be given to the candidate selected through valid GATE.</p>	<ul style="list-style-type: none"> <li>• Strong theoretical knowledge of Electrical fundamentals, Electrical Machines and Drives.</li> <li>• Experience with design of electrical motors and drive systems.</li> <li>• Proficiency in Finite Element software for electromagnetic, thermal, vibration and noise studies.</li> <li>• Proficiency is using embedded platform for motor control</li> <li>• First-hand experience with testing and characterization of electrical motors, converters and controllers</li> <li>• Scientific writing skills</li> </ul>	<ul style="list-style-type: none"> <li>• Design, prototype and execute experiments to validate PMA synchronous Reluctance motor and Switched reluctance motor.</li> <li>• Perform thermal FEA studies and suggest suitable cooling for the motor.</li> <li>• Analyze data and optimise the motor and controller design to</li> </ul>	<p>Initially for 1 year (Extendable up to 4 years subject to satisfactory performance and fund availability). Year 1 &amp; 2: ₹ 37,000/- + 30%HRA, Year 3, 4 &amp; 5: ₹ 42,000/- + 30% HRA</p>



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				(demonstrated by publications, if any).	meet the desired performance. • Draft scientific manuscripts for high-impact journals and present findings at conferences.	
02	GaN based DC-DC converters for next - generation EV power electronic subsystems: Research Innovations and Educational Resources	<b>02/P Asst-1</b> (No. of Post: 2)	B.E / B.Tech in Electrical / Electronic / Instrumentation Engineering	Candidate with relevant experience.	--	27,000/- + HRA (30%)
		<b>02/JRF-1</b> (No. of Post: 1)	M.Sc. in in Physics / Electronics OR Graduate/Post Graduate Degree in Engineering (B.E./B.Tech/M.E./M.Tech) in Electrical / Electronic / Instrumentation / Electronics and Telecommunication Engineering and equivalent	Candidate with relevant experience in DC-DC converters, power electronics and embedded systems	--	Monthly Emoluments: ₹ 37,000/- + 30%HRA
04	Sustainable Building: Developing an energy minimizing control of Variable Air Volume Air conditioning system	<b>04/JRF-1</b> (No. of Post: 1)	• Post Graduate Degree (M.E./ M.Tech / M.S.) in Control system, Power System, Electrical Engineering and equivalent and Scholars who are selected through GATE.	Candidate with relevant experience in embedded control system.	--	Monthly Emoluments: ₹ 37,000/- + 30%HRA



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	for Cooling applications					
09	Deepfake Detection using Deep Learning	09/NPDF-1 (No. of Post: 1)	<p>Eligibility:</p> <ul style="list-style-type: none"> <li>• The applicant should be an Indian citizen.</li> <li>• The applicant must have obtained Ph.D degree from a recognized University. Those who have submitted their PhD/M.D/M.S thesis and are awaiting award of the degree are also eligible to apply. However, such candidates, if selected, will be offered lower fellowship amount till they qualify the eligible degree.</li> <li>• The upper age limit for the fellowship is 35 years at the time of the submission of application; age will be calculated by taking the date of closure of the respective call. Age relaxation of 5 (five) years will be given to candidates belonging to SC/ST/OBC/Physically Challenged &amp; Women candidates.</li> <li>• NPDF can be availed only</li> </ul>	<ul style="list-style-type: none"> <li>• Proven publications in reputed journals and conferences in the field of image/video processing, cybersecurity, deep fake detection and other relevant research work.</li> <li>• Basic understanding of image/video processing, database systems</li> </ul>	<ul style="list-style-type: none"> <li>• Proven publications in reputed journals and conferences in the field of image/video processing, cybersecurity, deep fake detection and other relevant research work.</li> <li>• Basic understanding of image/video processing, database systems</li> <li>• Actively contribute to the research on Technologies for Digital Privacy and Security</li> <li>• Design and develop security solutions to detect the deepfakes in textual, image, video, audio formats</li> <li>• Publish research contributions in reputed journals and conferences</li> </ul>	₹ 80,000/- + ₹ 24000 HRA (30%) = ₹ 104000/- per month



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			once by a candidate in his/her career. Qualification: Ph.D. in Computer Science & Engineering/ Computer Engineering/ IT/Electronics and Telecommunication			
		<b>09/FA-1</b> (No. of Post: 1)	Essential Qualifications: • B.Sc. in Computer Science/ Electronics/ IT OR BCA OR • 3-Year Diploma in Computer Engineering / Information Technology / Electronics and Telecommunication.  Desirable Qualification: • BE / BTech in Computer Science & Engineering / Information Technology/Electronics and Telecommunication / Data Science or equivalent	<ul style="list-style-type: none"> <li>• Hands-on experience in Field work to gathering data in image/video format</li> <li>• Basic understanding of image/video processing, database systems</li> <li>• Basic computer proficiency for data entry</li> </ul>	<ul style="list-style-type: none"> <li>• Assisting the PIs and Research Scholars in the dataset creation, feature extraction</li> <li>• Visiting different industries for data collection</li> <li>• Design and execute experiments to capture and store the textual, image video information related to Deepfake detection.</li> <li>• Dataset generation suitable for Deepfake detection</li> </ul>	Duration: Initially for 1 year (Extendable up to 4 years subject to satisfactory performance and availability of funds). Monthly Emoluments: ₹ 27,000/- + 30% HRA
		<b>09/JRF-1</b> (No. of Post: 2)	• M.Sc. in Computer Science OR MCA OR Graduate/Post Graduate Degree in Engineering (B.E./B.Tech/M.E./M.Tech) in	GATE Qualified candidates in Computer Science & Engineering OR Data Science & Artificial Intelligence • Hands-on skill of python	Conduct comprehensive literature review on Deepfake Detection	Year 1 & 2: ₹ 37,000/- + 30%HRA, Year 3: ₹ 42,000/- + 30% HRA (Subject to



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			Computer Engineering / Information Technology / Computer Science & Engineering / Electronics and Telecommunication and equivalent.	programming, AI framework such as TensorFlow/ PyTorch <ul style="list-style-type: none"> <li>• Strong theoretical knowledge of Artificial Intelligence, Machine Learning, &amp; DL Data Analytics, Video Analytics, Mathematical fundamentals pertaining to AI</li> <li>• Knowledge and experience in developing Cybersecurity solutions</li> <li>• Experience of deploying AI models for experimentation on Nvidia Cards</li> <li>• Scientific writing skills (demonstrated by publications, if any).</li> </ul>	(multimodal) and perform gap analysis.  Develop and propose novel solutions for identified cyber security challenges.  Design and execute experiments using various Deep Learning (DL) models. Deploy and evaluate models on GPU systems using diverse datasets.  Draft scientific manuscripts for high-impact journal publication.	completion of two years and a successful external assessment as per institutional norms)
		<b>09/SRF-1</b> <b>(No. of Post: 1)</b>	<ul style="list-style-type: none"> <li>• M.Sc. in Computer Science / MCA with proven research publication in the field of Information &amp; Cyber Security OR</li> <li>• Graduate/Post Graduate Degree in Engineering/ Technology (B.E./B.Tech/M.E./M.Tech) in</li> </ul>	<ul style="list-style-type: none"> <li>• GATE Qualified candidates in Computer Science &amp; Engineering OR Data Science &amp; Artificial Intelligence AND having minimum experience as JRF will be preferred</li> <li>• Hands-on skill of python programming, AI framework such as TensorFlow/ PyTorch</li> <li>• Strong theoretical knowledge of</li> </ul>	<ul style="list-style-type: none"> <li>• Survey Deepfake Detection (multimodal) and identify gaps.</li> <li>• Propose novel solutions for identified cyber security issues.</li> <li>• Deploy and experimentally validate</li> </ul>	₹ 42,000/- + 30%HRA Initially for a period of 1 year further extendable up to additional 4 years subject to satisfactory performance and



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			<p>Computer Engineering / Information Technology / Computer Science &amp; Engineering / Electronics and Telecommunication.</p> <p>OR</p> <ul style="list-style-type: none"> <li>Registered for PhD in Computer Science or relevant branch and nearing completion with publication in SCI journals in the domain of Information &amp; Cyber security</li> </ul>	<p>Artificial Intelligence, Machine Learning, &amp; DL Data Analytics, Video Analytics, Mathematical fundamentals pertaining to AI</p> <ul style="list-style-type: none"> <li>Knowledge and experience in developing Cybersecurity solutions</li> <li>First-hand experience Nvidia Cards, deploying AI models for experimentation</li> <li>Scientific writing skills (demonstrated by publications, if any).</li> </ul>	<p>DL models on GPU systems.</p> <ul style="list-style-type: none"> <li>Publish findings in high-impact journals and present at conferences.</li> </ul>	<p>availability of funds.</p>
	<p><b>09/P Asso-1</b> <b>(No. of Post: 1)</b></p>	<ul style="list-style-type: none"> <li>M.Sc. in Computer Science OR MCA OR</li> <li>Graduate/Post Graduate Degree in Engineering (B.E./B.Tech/M.E./M.Tech) in Computer Engineering / Information Technology / Computer Science &amp; Engineering / Electronics and Telecommunication</li> </ul>	<ul style="list-style-type: none"> <li>GATE Qualified candidates in Computer Science &amp; Engineering OR Data Science &amp; Artificial Intelligence</li> <li>Hands-on skill of python programming, AI framework such as TensorFlow/ PyTorch</li> <li>Strong theoretical knowledge of Artificial Intelligence, Machine Learning, &amp; DL Data Analytics, Video Analytics, Mathematical fundamentals pertaining to AI</li> <li>Knowledge and experience in developing Cybersecurity solutions</li> <li>Experience of deploying AI models for experimentation on Nvidia Cards</li> <li>Scientific writing skills</li> </ul>	<ul style="list-style-type: none"> <li>Conduct exhaustive literature survey on Deepfake Detection on multimodal datasets and identify the challenges in existing state of the work</li> <li>Propose new solutions to address the various identified issues in cyber security</li> <li>Experiment with different DL models to evaluate the performance of the proposed solution.</li> <li>Deploy the models on</li> </ul>	<p>₹ 37,000/- + 30%HRA Initially for 1 year (Extendable up to additional 4 years subject to satisfactory performance and fund availability).</p>	



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				(demonstrated by publications, if any).	GPU systems and evaluate the proposed model on variety of datasets • Draft scientific manuscripts for high-impact journals and present findings at conferences.	
10	Enabling Smart Machining Through Digital Twins: A Scalable Framework for Condition Monitoring, Process Optimization, and Zero-Downtime Manufacturing.	10/SRF-1 (No. of Post: 1)	Post Graduate Degree (M-Tech/ME) in Mechanical Engineering/ Computer Science Engineering/Data Science or aligned engineering branches with minimum 1 year research experience.  * Preference will be given to candidates with:  1. Graduation Degree (B.E/B.Tech) in Mechanical Engineering/ Computer Science Engineering  2. Valid Gate Score *Subject to his/her satisfactory performance in interviews.	<ul style="list-style-type: none"> <li>• Strong theoretical grounding in AI/ML, data analytics, video analytics, and essential mathematical concepts including linear algebra, probability, and optimization.</li> <li>• Hands-on experience in Python, deep learning frameworks (TensorFlow, PyTorch), and building end-to-end ML/DL pipelines for real-time applications.</li> <li>• Practical expertise with NVIDIA Jetson platforms and cloud-based AI/ML deployment (Docker, ONNX, edge-cloud integration) for implementing intelligent Digital Twin systems.</li> </ul>	<ul style="list-style-type: none"> <li>• Design/analyze experiments for multi-stream machining sensor data capture (Vibration, Force, Video).</li> <li>• Curate and publish high-quality datasets for machining condition monitoring.</li> <li>• Develop/optimize AI/ML models for zero-downtime manufacturing analysis.</li> <li>• Deploy AI/ML models within Digital Twin</li> </ul>	₹ 42,000/- + 30%HRA Initially for a period of 1 year further extendable up to additional 4 years subject to satisfactory performance and availability of funds.



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				<ul style="list-style-type: none"><li>• Proven capability in scientific writing, research reporting, and applying AI/ML models for digital twin-based monitoring, prediction, and decision-support in smart manufacturing</li></ul>	<ul style="list-style-type: none"><li>framework for predictive monitoring.</li><li>• Prepare manuscripts, reports, and documentation; present research findings.</li></ul>	
<b>Total Number of Post</b>	<b>13</b>					

**Candidates who do not meet the mandatory NET/GATE or equivalent qualifications for JRF/SRF positions may be considered for an appropriate lower designation. In such instances, designation and emoluments will be governed by the prevailing institutional and funding agency guidelines.**

Candidates must apply in the prescribed **Application form** available on COEP Technological University website "[www.coeptech.ac.in](http://www.coeptech.ac.in)" under "**Careers**" tab.

## Other Terms and Conditions

1. All positions are purely temporary in nature and are co-terminus with the ANRF-PAIR Project (2025–2030) or until completion of the project, or as per availability of funds.
2. These appointments do not confer any right to regular or permanent employment at COEP Technological University, Pune.
3. The post shall be treated as *non-vacational*, as applicable, in accordance with the norms of COEP Technological University and ANRF.
4. The appointment order shall be issued only after due vetting and approval by the competent legal authority of COEP Technological University.



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5. Selected candidates shall be required to submit an affidavit at the time of joining, affirming acceptance of all terms and conditions of the project appointment.
6. COEP Technological University reserves the right to modify, restrict, or cancel the recruitment process, any application, or any post without assigning any reason thereof.
7. Applications submitted without required testimonials or with incomplete information are liable to be rejected.
8. No Travelling Allowance (TA) or Dearness Allowance (DA) shall be paid for attending the interview.
9. Shortlisted candidates will be notified via their registered email ID, and the final list of candidates called for interview will also be displayed on the University website.
10. Applications in the prescribed format, along with photocopies of relevant testimonials be submitted in an envelope superscribed as:  
**"APPLICATION FOR RESEARCH POSITION (POST CODE:        ) UNDER ANRF-PAIR Programme"**

**For Example:** *"APPLICATION FOR RESEARCH POSITION (POST CODE:01/JRF-1) UNDER ANRF-PAIR Programme"*

11. Candidates applying for multiple posts should send separate application for each post they have applied for.
12. **Submission of Application** The completed application form must reach through **Registered Post / Speed Post / Courier** latest by **24<sup>th</sup> April 2026 till 5:00 pm** addressed to:

To,  
The Registrar,  
COEP Technological University,  
Wellesley Road, Shivajinagar,



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*Pune – 411 005,  
Maharashtra, INDIA.*

## 13. Clarifications

For any clarifications or queries related to the recruitment, candidates may contact via email at [mohitess.mech@coeptech.ac.in](mailto:mohitess.mech@coeptech.ac.in).

**Place: Pune 411005**  
**Date: 16/03/2026**

**Sd/-  
Registrar  
COEP Technological University Pune**



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## **ADVERTISEMENT FOR PROJECT POSITIONS**

Applications are invited from eligible Indian Nationals for various project positions under the ANRF-PAIR project IIT-B COEP-TU Hub-Spoke model through the Partnerships for Accelerated Innovation and Research (PAIR) programme of the Anusandhan National Research Foundation (ANRF), New Delhi. The sanctioned posts, number of vacancies and emoluments are listed below for different research projects at COEP Technological University, Pune. **The detailed advertisement has been published on the institute website on the date 16/03/2025.** Applicants can access the same under "Career" tab on the website [www.coeptech.ac.in](http://www.coeptech.ac.in)

Name of Post	National Post Doctoral Fellowship (NPDF)	Junior Research Fellow (JRF)		Senior Research Fellow (SRF)	Field Assistant (FA)	Project Assistant (P Asst.)		Research Associate-I (RA-I)	Project Associate (P Asso.)
No. of Post	1	1	4	2	1	1	1	1	1
Duration* (In Years)	2	4.5	5	5	5	4	5	5	5
Emoluments Rs/Month	80,000/-	37,000/-		42,000/-	27,000/-	27,000/-		58,000/-	37,000/-

# + 30% HRA is applicable for all posts except Senior Project Manager

Tenure: Purely on temporary basis, non-vacational and co-terminus with the ANRF-PAIR Project (2025-2030).

**Last date: Completed application form must reach on or before 24-04-2026 (till 5:00 PM).** For complete details and other terms and conditions visit [www.coeptech.ac.in](http://www.coeptech.ac.in) and navigate to the **Career** section.

Pune: 411005

Sd/-

Date: 16/03/2025

Registrar